# MICROPROCESSOR BCA 3RD SEMESTER 2020 LECTURE- 10

#### **SUBHADIP MUKHERJEE**

DEPARTMENT OF COMPUTER SCIENCE
KHARAGPUR COLLEGE

- DATA TRANSFER INSTRUCTIONS
- ARITHMETIC INSTRUCTIONS
- BRANCHING INSTRUCTIONS
- LOGICAL INSTRUCTIONS
- CONTROL INSTRUCTIONS

#### **BRANCHING INSTRUCTIONS (Cont.)**

#### **RET Instruction**

Return from subroutine unconditionally RET none

Return from subroutine conditionally

Operand: none

#### **BRANCHING INSTRUCTIONS (Cont.)**

#### **PCHL** Instruction

PCHL none

Example: PCHL

#### **BRANCHING INSTRUCTIONS (Cont.)**

#### **RST** Instruction

RST 0-7

Restart Address
0000H
H8000
0010H
0018H
0020H
0028H
0030H
0038H

Interrupt	Restart Address
TRAP	0024H
RST 5.5	002CH
RST 6.5	0034H
RST 7.5	003CH

#### **LOGICAL INSTRUCTIONS**

Compare register or memory with accumulator

CMP R M

Example: CMP B or CMP M

Compare immediate with accumulator

CPI 8-bit data

Example: CPI 89H

**LOGICAL INSTRUCTIONS (Cont.)** 

Logical AND register or memory with accumulator

ANA R M

Example: ANA B or ANA M

Logical AND immediate with accumulator

ANI 8-bit data

Example: ANI 86H

**LOGICAL INSTRUCTIONS (Cont.)** 

**Exclusive OR register or memory with accumulator** 

XRA R M

Example: XRA B or XRA M

**Exclusive OR immediate with accumulator** 

XRI 8-bit data

Example: XRI 86H

**LOGICAL INSTRUCTIONS (Cont.)** 

Logical OR register or memory with accumulator

ORA R M

Example: ORA B or ORA M

Logical OR immediate with accumulator

ORI 8-bit data

Example: ORI 86H

**LOGICAL INSTRUCTIONS (Cont.)** 

Rotate accumulator left

RLC none

Example: RLC

Rotate accumulator right

RRC none

Example: RRC

**LOGICAL INSTRUCTIONS (Cont.)** 

Rotate accumulator left through carry

RAL none

Example: RAL

Rotate accumulator right through carry

RAR none

Example: RAR

**LOGICAL INSTRUCTIONS (Cont.)** 

Complement accumulator Set Carry

CMA none STC none

Example: CMA Example: STC

**Complement carry** 

CMC none

Example: CMC

#### **CONTROL INSTRUCTIONS**

No operation

NOP none

Example: NOP

Halt and enter wait state

HLT none

Example: HLT

#### **CONTROL INSTRUCTIONS**

**Disable interrupts** 

DI none

Example: DI

**Enable interrupts** 

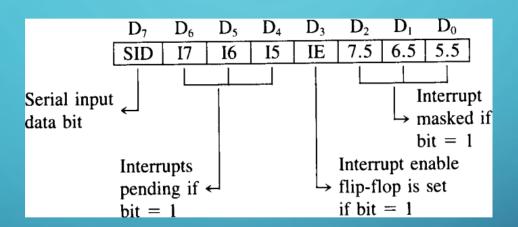
El none

**Example: El** 

#### **CONTROL INSTRUCTIONS**

#### Read interrupt mask

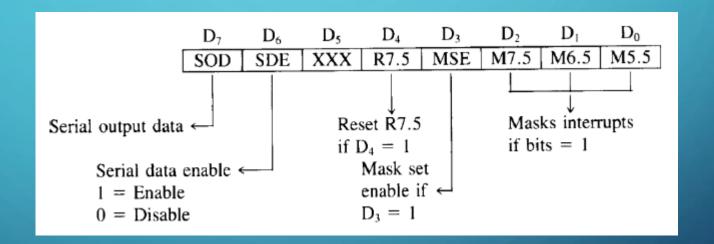
RIM none



#### **CONTROL INSTRUCTIONS**

#### Set interrupt mask

SIM none



# **THANK YOU**

End of Lecture- 10